

## SIPMOS® Power-Transistor

### Feature

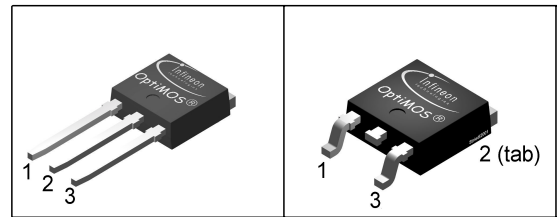
- N-Channel
- Enhancement mode
- 175°C operating temperature
- Avalanche rated
- dv/dt rated
- Ideal for fast switching buck converter

### Product Summary

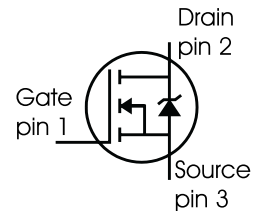
$V_{DS}$	100	V
$R_{DS(on)}$	170	mΩ
$I_D$	10.5	A

P- TO251 -3-1

P- TO252 -3-11



Type	Package	Ordering Code	Marking
SPD11N10	P- TO252 -3-11	Q67042-S4121	11N10
SPU11N10	P- TO251 -3-1	Q67042-S4122	11N10



### Maximum Ratings, at $T_j = 25\text{ °C}$ , unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current $T_C=25\text{ °C}$	$I_D$	10.5 7.8	A
Pulsed drain current $T_C=25\text{ °C}$	$I_{D\text{ puls}}$	41.2	
Avalanche energy, single pulse $I_D=10.5\text{ A}$ , $V_{DD}=25\text{ V}$ , $R_{GS}=25\text{ Ω}$	$E_{AS}$	60	mJ
Reverse diode dv/dt $I_S=10.5\text{ A}$ , $V_{DS}=80\text{ V}$ , $di/dt=200\text{ A/μs}$ , $T_{jmax}=175\text{ °C}$	dv/dt	6	kV/μs
Gate source voltage	$V_{GS}$	±20	V
Power dissipation $T_C=25\text{ °C}$	$P_{tot}$	50	W
Operating and storage temperature	$T_j, T_{stg}$	-55... +175	°C
IEC climatic category; DIN IEC 68-1		55/175/56	

**Thermal Characteristics**

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
<b>Characteristics</b>					
Thermal resistance, junction - case	$R_{thJC}$	-	-	3	K/W
Thermal resistance, junction - ambient, leaded	$R_{thJA}$	-	-	100	
SMD version, device on PCB:	$R_{thJA}$				
@ min. footprint		-	-	75	
@ 6 cm <sup>2</sup> cooling area <sup>1)</sup>		-	-	50	

**Electrical Characteristics**, at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
<b>Static Characteristics</b>					
Drain-source breakdown voltage $V_{GS}=0V, I_D=1mA$	$V_{(BR)DSS}$	100	-	-	V
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D = 21\ \mu A$	$V_{GS(th)}$	2.1	3	4	
Zero gate voltage drain current $V_{DS}=100V, V_{GS}=0V, T_j=25^\circ C$ $V_{DS}=100V, V_{GS}=0V, T_j=125^\circ C$	$I_{DSS}$	-	0.01	1	$\mu A$
		-	1	100	
Gate-source leakage current $V_{GS}=20V, V_{DS}=0V$	$I_{GSS}$	-	1	100	nA
Drain-source on-state resistance $V_{GS}=10V, I_D=7.8A$	$R_{DS(on)}$	-	137	170	m $\Omega$

<sup>1)</sup>Device on 40mm\*40mm\*1.5mm epoxy PCB FR4 with 6cm<sup>2</sup> (one layer, 70  $\mu m$  thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics, at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

### Dynamic Characteristics

Transconductance	$g_{fs}$	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$ , $I_D = 7.8\text{A}$	2.6	5.8	-	S
Input capacitance	$C_{iss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$	-	320	400	pF
Output capacitance	$C_{oss}$		-	72	90	
Reverse transfer capacitance	$C_{rss}$		-	43	54	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 50\text{V}$ , $V_{GS} = 10\text{V}$ , $I_D = 10.5\text{A}$ , $R_G = 28\Omega$	-	8.2	10	ns
Rise time	$t_r$		-	46	58	
Turn-off delay time	$t_{d(off)}$		-	29	36	
Fall time	$t_f$		-	23	29	

### Gate Charge Characteristics

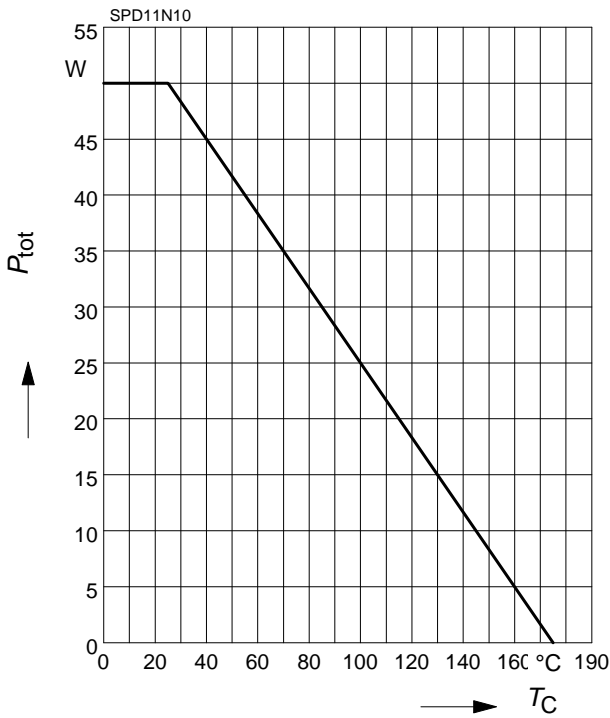
Gate to source charge	$Q_{gs}$	$V_{DD} = 80\text{V}$ , $I_D = 10.5\text{A}$	-	2.3	2.9	nC
Gate to drain charge	$Q_{gd}$		-	7.8	9.8	
Gate charge total	$Q_g$	$V_{DD} = 80\text{V}$ , $I_D = 10.5\text{A}$ , $V_{GS} = 0$ to $10\text{V}$	-	14.6	18.3	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 80\text{V}$ , $I_D = 10.5\text{A}$	-	6.4	-	V

### Reverse Diode

Inverse diode continuous forward current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$	-	-	10.5	A
Inv. diode direct current, pulsed	$I_{SM}$		-	-	41.2	
Inverse diode forward voltage	$V_{SD}$	$V_{GS} = 0\text{V}$ , $I_F = 10.5\text{A}$	-	0.93	1.25	V
Reverse recovery time	$t_{rr}$	$V_R = 50\text{V}$ , $I_F = I_S$ , $di_F/dt = 100\text{A}/\mu\text{s}$	-	57	71	ns
Reverse recovery charge	$Q_{rr}$		-	134	167	

### 1 Power dissipation

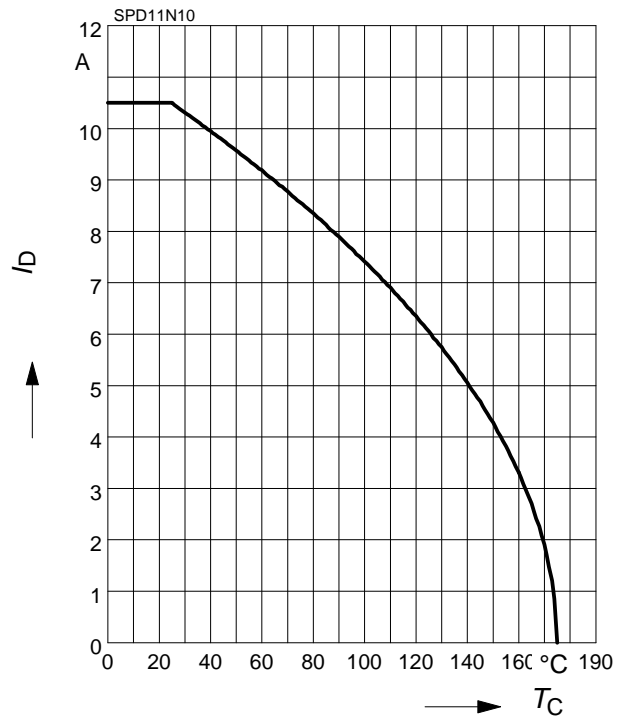
$$P_{tot} = f(T_C)$$



### 2 Drain current

$$I_D = f(T_C)$$

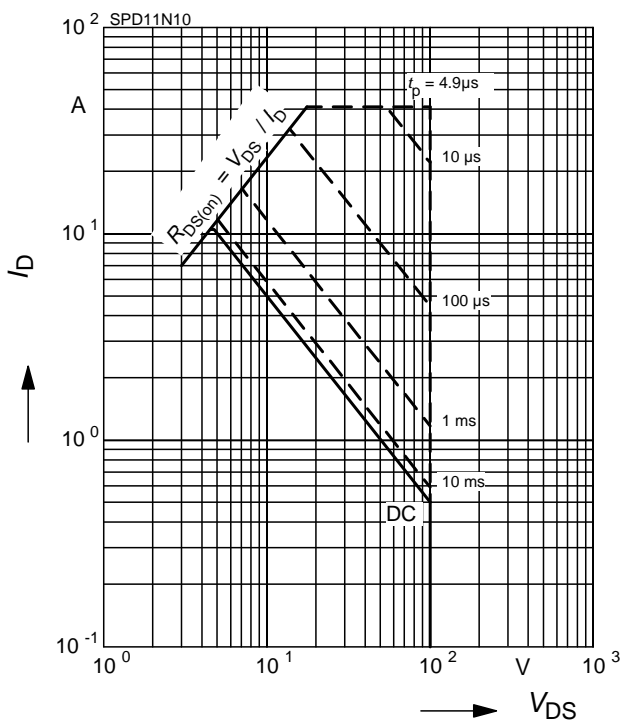
parameter:  $V_{GS} \geq 10 \text{ V}$



### 3 Safe operating area

$$I_D = f(V_{DS})$$

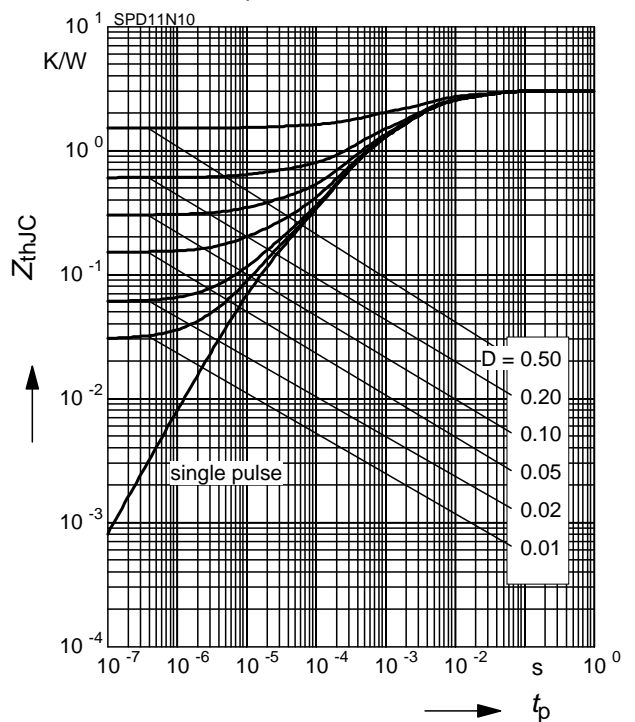
parameter:  $D = 0$ ,  $T_C = 25 \text{ °C}$



### 4 Max. transient thermal impedance

$$Z_{thJC} = f(t_p)$$

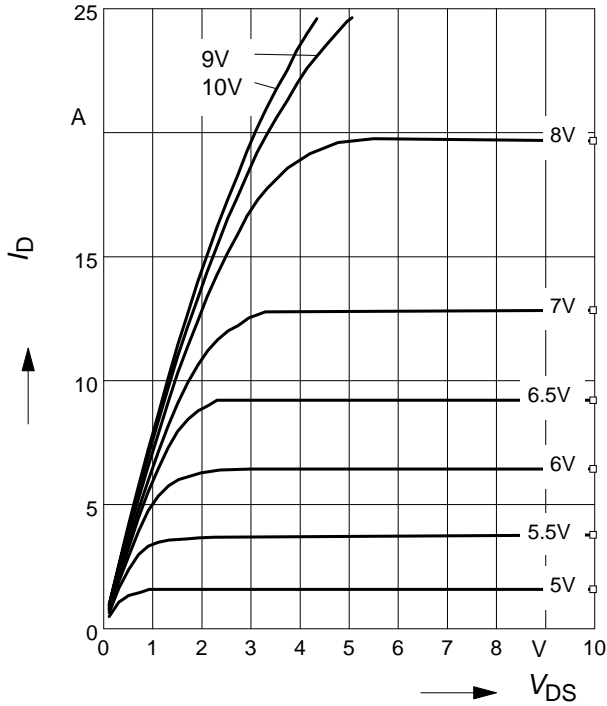
parameter:  $D = t_p/T$



**5 Typ. output characteristic**

$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$

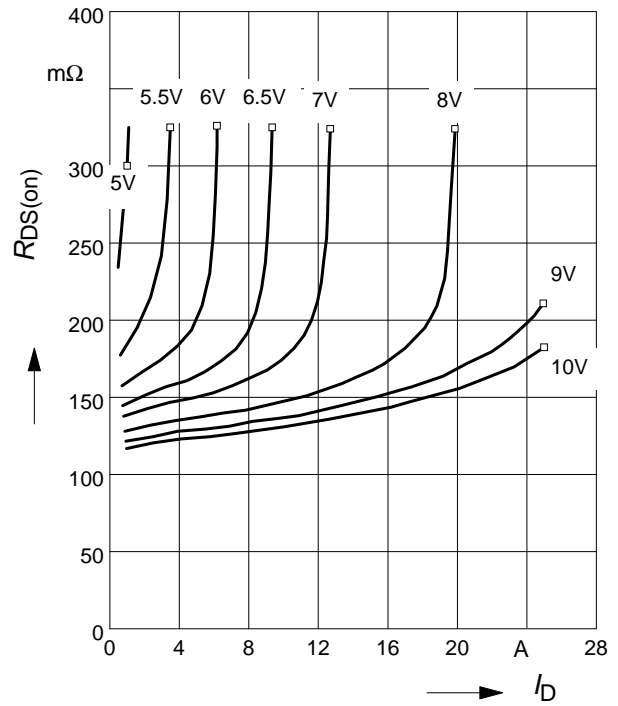
parameter:  $t_p = 80 \mu\text{s}$



**6 Typ. drain-source on resistance**

$R_{DS(on)} = f(I_D)$

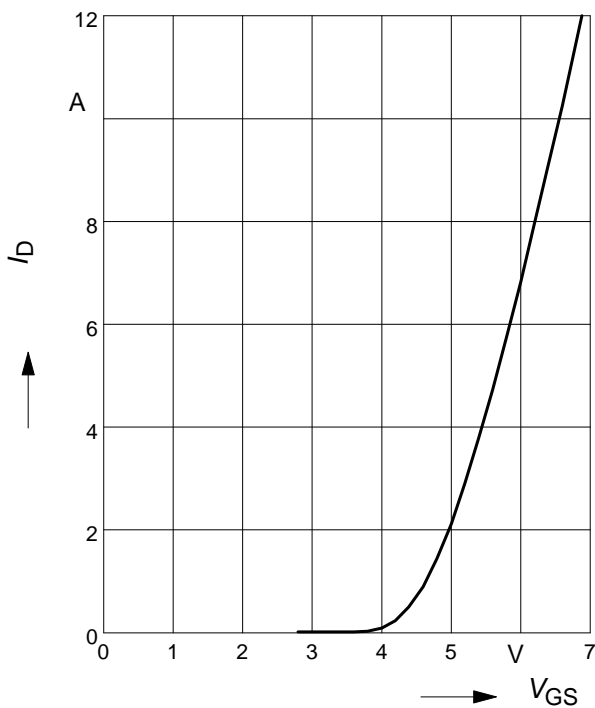
parameter:  $V_{GS}$



**7 Typ. transfer characteristics**

$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$

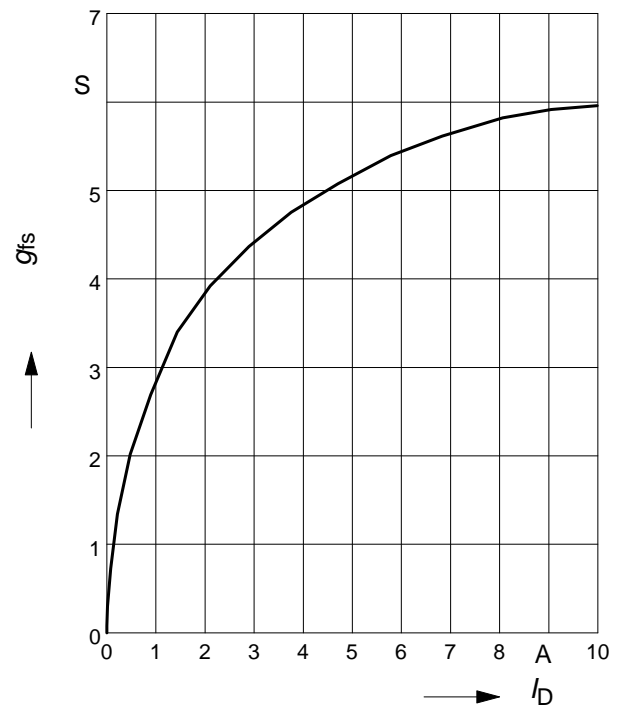
parameter:  $t_p = 80 \mu\text{s}$



**8 Typ. forward transconductance**

$g_{fs} = f(I_D); T_j = 25^\circ\text{C}$

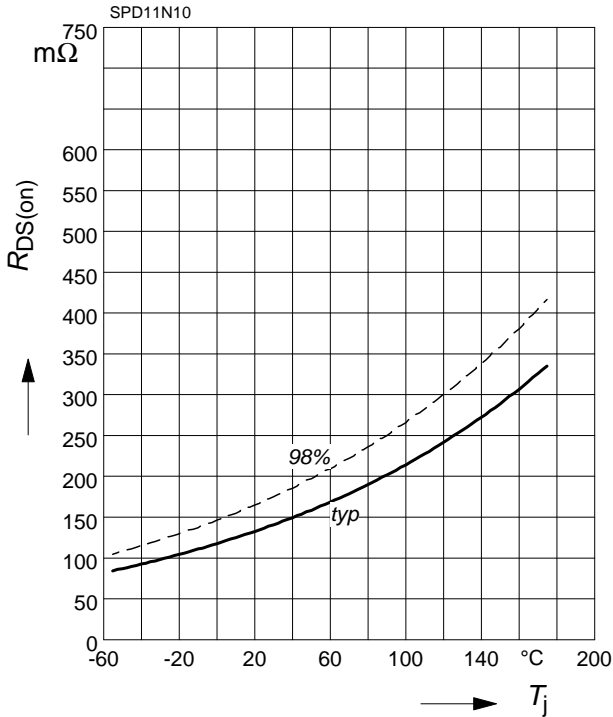
parameter:  $g_{fs}$



**9 Drain-source on-state resistance**

$$R_{DS(on)} = f(T_j)$$

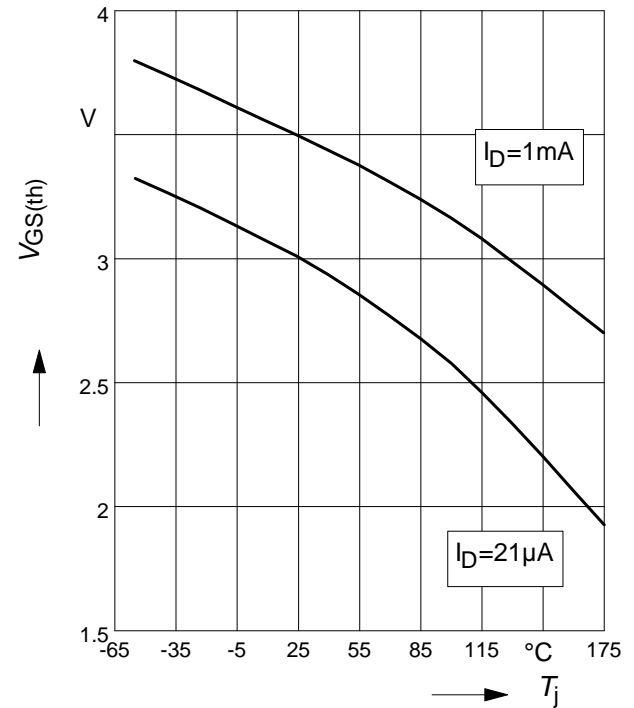
parameter:  $I_D = 7.8 \text{ A}$ ,  $V_{GS} = 10 \text{ V}$



**10 Typ. gate threshold voltage**

$$V_{GS(th)} = f(T_j)$$

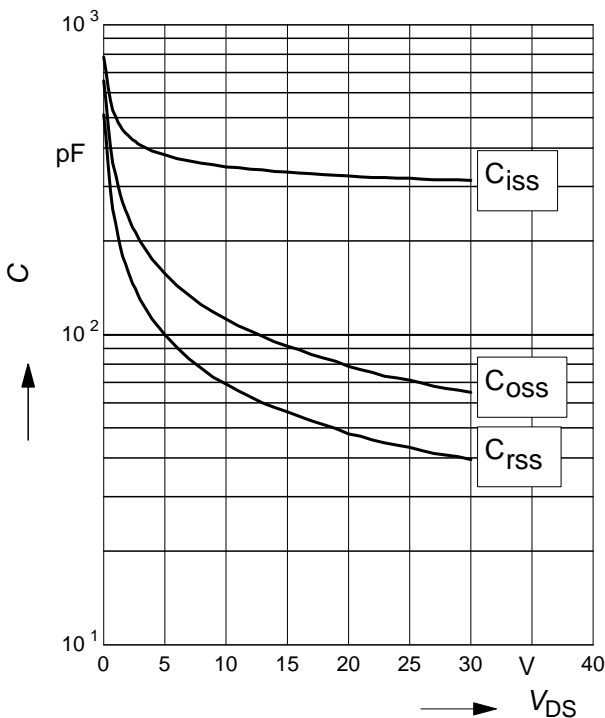
parameter:  $V_{GS} = V_{DS}$



**11 Typ. capacitances**

$$C = f(V_{DS})$$

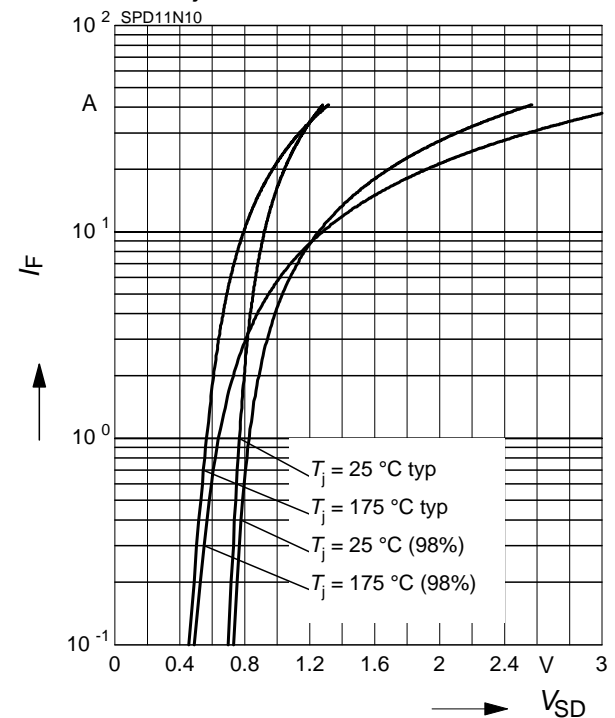
parameter:  $V_{GS} = 0 \text{ V}$ ,  $f = 1 \text{ MHz}$



**12 Forward character. of reverse diode**

$$I_F = f(V_{SD})$$

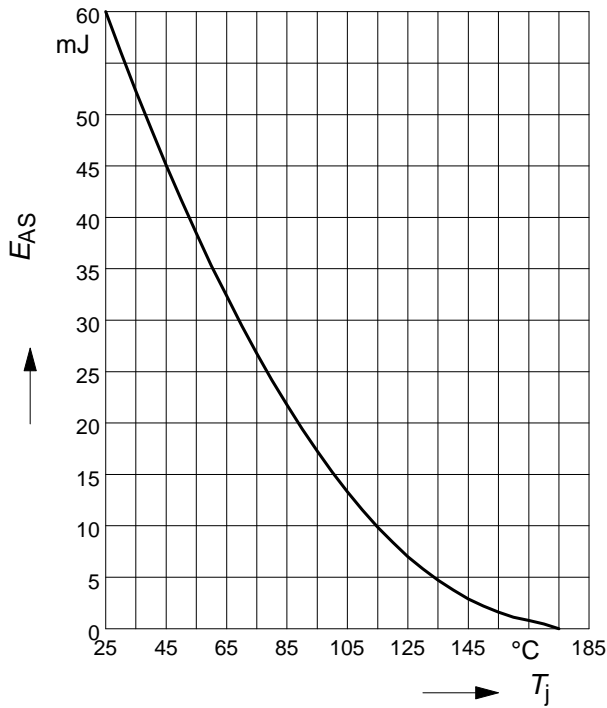
parameter:  $T_j$ ,  $t_p = 80 \mu\text{s}$



**13 Typ. avalanche energy**

$$E_{AS} = f(T_j)$$

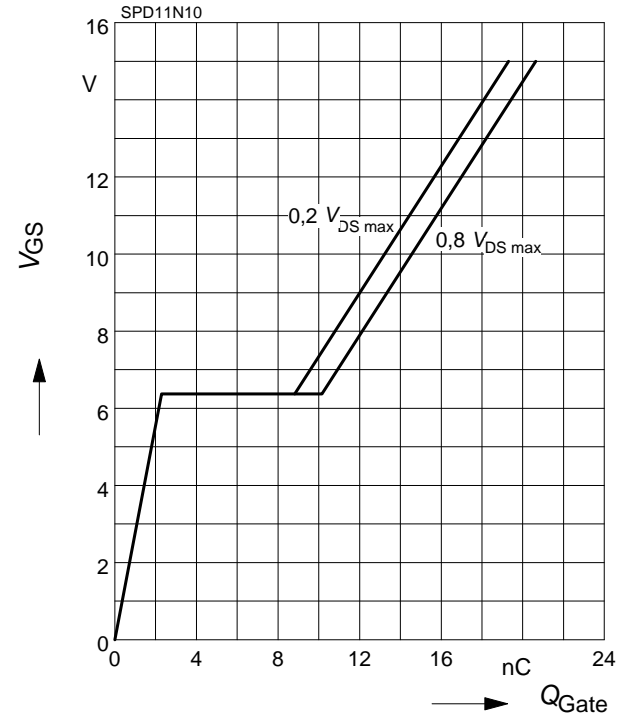
par.:  $I_D = 10.5 \text{ A}$  ,  $V_{DD} = 25 \text{ V}$  ,  $R_{GS} = 25 \Omega$



**14 Typ. gate charge**

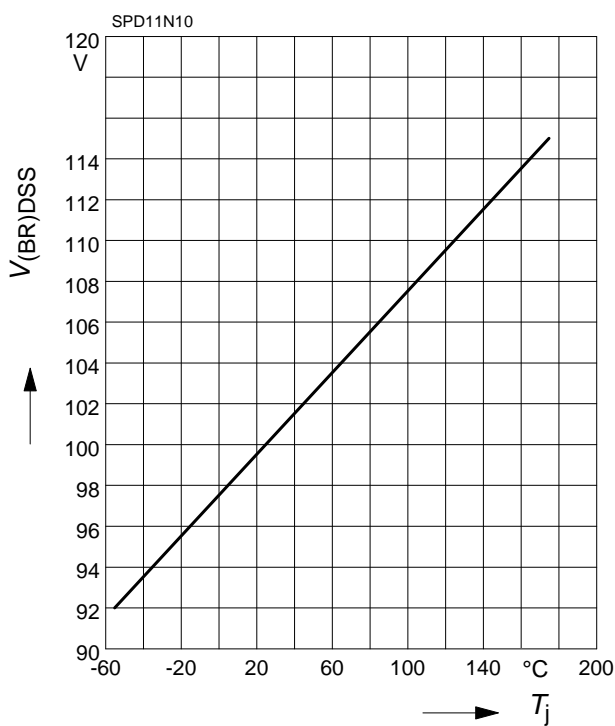
$$V_{GS} = f(Q_{Gate})$$

parameter:  $I_D = 10.5 \text{ A}$  pulsed



**15 Drain-source breakdown voltage**

$$V_{(BR)DSS} = f(T_j)$$



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